

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	Confirmation No. 5184
Yasuyuki ARAI et al.)	Group Art Unit: 2811
Serial No. 10/781,795)	Examiner: Colleen A. Matthews
Filed: February 20, 2004)	
For: THIN FILM INTEGRATED CIRCUIT DEVICE,)	
IC LABEL, CONTAINER COMPRISING THE)	
THIN FILM INTEGRATED CIRCUIT,)	
MANUFACTURING METHOD OF THE THIN)	
FILM INTEGRATED CIRCUIT DEVICE,)	
MANUFACTURING METHOD OF THE)	
CONTAINER, AND MANAGEMENT)	
METHOD OF PRODUCT HAVING THE)	
CONTAINER)	

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Copies of U.S. patents and U.S. publications are not enclosed in accordance with the Notice published in the Official Gazette on August 5, 2003 entitled *Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003*, which waives the requirement under 37 C.F.R. 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. publication.

U.S. Patent No. 7,532,018 is in the family of U.S. Patent No. 6,850,080.


U.S. Publication No. 2006/0263952 is in the family of 7,105,365.

U.S. Publication No. 2005/0218926 is in the family of U.S. Patent No. 6,891,391.

This Information Disclosure Statement is being submitted with an RCE.
Therefore, no fee is required.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,



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